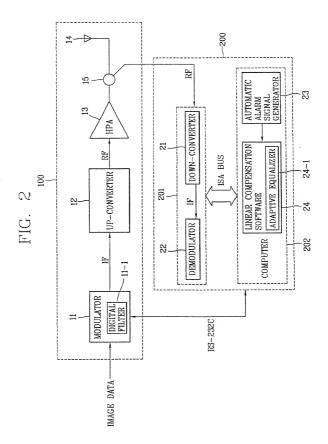
RF -30 DOWN-CONVERTER HPA GPIB INTERFACE LINEAR COMPENSATION SOFTWARE 胚 FIG. 1 BACKGROUND ART UP-CONVERTER 20 급 DEMODULATOR COMPUTER ADAPTIVE EQUALIZER MODULATOR DIGITAL RS-232C IMAGE DATA





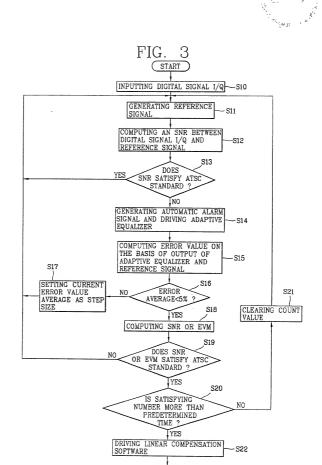


FIG. 4A

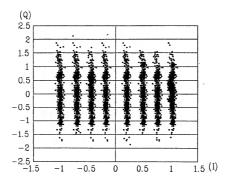


FIG. 4B

